JAN 1 8 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Jason Cheng; Cyrus Tsui; Satwant Singh; Albert Chan; Ju Shen;

Clement Lee

Assignee:

Lattice Semiconductor Corporation

Title:

Programmable Logic Device With Enhanced Wide Input Product

Term Cascading

Application No.:

10/619,711

Filing Date:

July 14, 2003

Examiner:

James Cho

Group Art Unit:

2819

Docket No.:

M-9559-2P US

Confirmation No.:

7093

Irvine, California January 18, 2005

Via Facsimile to (703) 872-9306

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following documents are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below:

1) Transmittal Letter (1 page); and

2) Response to Office Action (9 pages).

Dated: January 18, 2005

Jonathan W. Hallman

Registration No. 42,622

Number of pages (including this sheet): _____11

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Re:

Applicants:

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Examiner: Docket No.: James Hyonchol Cho M-9559-2P US Group Art Unit: Confirmation No.: 2819 7093

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Certification of Facsimile Transmission (1 page);
- (2) This Transmittal Letter (1 page); and
- (3) Response to Office Action (9 pages).

No additional fee is required.

The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining After Amendment		Highest No. Previously Paid For	•	Present Extra		Rate		Additional Fee
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Informati	on Disclosure Statemer	nt	•					\$	
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Office on the date shown below.

Linda Bolter

January 18, 2005
Date of Signature

Respectfully submitted,

Jonathan W. Hallman Attorney for Applicants

Reg. No. 42,622

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RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed October 18, 2004, please amend the above-identified application as follows.

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